

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|--|------------------|---------|------------------|
| S1 | 528 | (381/2).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/11/13 11:53 |
| S2 | 765 | (381/77).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:55 |
| S3 | 324 | suzuki.in. and broadcast\$3 and pattern | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/11/13 11:25 |
| S4 | 62 | suzuki.in. and broadcast\$3 and pattern and rout\$3 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 19:47 |
| S5 | 0 | toa.AS. and broadcast\$3 and pattern and rout\$3 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/11/13 11:36 |
| S6 | 952 | (370/462).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/11/13 11:53 |
| S7 | 2 | S1 and (broadcast\$3 near3 (pattern patterns)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/11/13 11:54 |
| S8 | 1 | S2 and (broadcast\$3 near3 (pattern patterns)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:05 |
| S9 | 4 | S6 and (broadcast\$3 near3 (signature pattern patterns)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/11/13 11:55 |

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|-----|----|---|--|----|-----|------------------|
| S10 | 63 | suzuki.in. and broadcast\$3 and pattern and rout\$3 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 19:47 |
| S11 | 0 | S10 and patterns same control\$3 same intial\$3 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 19:47 |
| S12 | 10 | S10 and (patterns same control\$3) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:06 |
| S13 | 1 | (broadcast\$3 near3 system) same (control\$3 with forces with input with devices with register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:07 |
| S14 | 1 | (broadcast\$3 near3 system) and (control\$3 with forces with input with devices with register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:07 |
| S15 | 1 | (broadcast\$3) and (control\$3 with forces with input with devices with register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:08 |
| S16 | 84 | (broadcast\$3) and (control\$3 with input with devices with register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:09 |
| S17 | 1 | (broadcast\$3) and (forc\$3 near4 input near3 devices near3 register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:10 |
| S18 | 35 | (broadcast\$3) and (input near3 devices near3 register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:11 |
| S19 | 1 | (broadcast\$3 with patterns) and (input near3 devices near3 register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:12 |

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| S20 | 1 | (broadcast\$3 with patterns) and ((input near3 devices) with register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:12 |
| S21 | 1 | (broadcast\$3 with patterns) and ((input near3 devices) same register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:25 |
| S22 | 4582 | digital near2 broadcast\$3 near2 (apparatus system) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:05 |
| S23 | 18 | (digital near2 broadcast\$3 near2 (apparatus system)) same patterns | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:14 |
| S24 | 8 | (digital near2 broadcast\$3 near2 (apparatus system)) and (register\$3 with patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:15 |
| S25 | 680 | (broadcasting) and (input near3 devices) and (output near2 devices) and patterns | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:26 |
| S26 | 3 | (broadcasting) same (input near3 devices) same (output near2 devices) and patterns | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:26 |
| S27 | 10 | (broadcasting) and ((input near3 devices) same (output near2 devices) same patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:26 |
| S28 | 531 | (381/2).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:41 |
| S29 | 768 | (381/77).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:55 |

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| S30 | 223 | S29 and broadcast\$3 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:56 |
| S31 | 957 | (370/462).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:18 |
| S32 | 1 | ("5885129").PN. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:38 |
| S33 | 1444 | (370/351,351).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:39 |
| S34 | 3899 | (370/351,360).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 12:45 |
| S35 | 1446 | (370/390,432).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:39 |
| S36 | 29 | "714"/\$.ccls. and (broadcast\$3) and (pattern with matching) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:40 |
| S37 | 2174 | (370/432,485,486,487,492).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:23 |
| S38 | 9303 | (709/217,218,219).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 13:30 |
| S39 | 0 | S37 and (input with devices with register\$3 with broadcast\$3 with (blueprint design model convention pattern)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:02 |

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| S40 | 0 | S37 and (input with devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:02 |
| S41 | 0 | S37 and (devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:02 |
| S42 | 110 | (devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:07 |
| S43 | 1 | (input near2 devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:07 |
| S44 | 4593 | (370/432,485,486,487,492,462,351).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:23 |
| S45 | 0 | S44 and (controller) with devices with register\$3 with (blueprint design model convention pattern patterns) | USPAT | OR | OFF | 2007/02/02 14:25 |
| S46 | 1 | S44 and (controller) with devices with register\$3 with (blueprint design model convention pattern patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:25 |
| S47 | 1 | S44 and (controller) with devices with (store register\$3 receive) with (blueprint design model convention pattern patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:31 |
| S48 | 1 | S44 and (controller) with devices with (store register\$3 receive) with input with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:31 |
| S49 | 0 | S38 and (controller) with devices with (store register\$3 receive) with input with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:32 |

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| S50 | 12 | (controller) with devices with (store register\$3 receive) with input with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:38 |
| S51 | 3 | (controller) with devices with (store register\$3 receive) with source with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:38 |

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| S16 | 84 | (broadcast\$3) and (control\$3 with input with devices with register) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/20 20:09 |
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| S24 | 8 | (digital near2 broadcast\$3 near2 (apparatus system)) and (register\$3 with patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:15 |
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| S26 | 3 | (broadcasting) same (input near3 devices) same (output near2 devices) and patterns | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:26 |
| S27 | 10 | (broadcasting) and ((input near3 devices) same (output near2 devices) same patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:26 |
| S28 | 531 | (381/2).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:41 |
| S29 | 768 | (381/77).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 09:55 |

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| S31 | 957 | (370/462).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:18 |
| S32 | 1 | ("5885129").PN. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:38 |
| S33 | 1444 | (370/351,351).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:39 |
| S34 | 3899 | (370/351,360).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 12:45 |
| S35 | 1446 | (370/390,432).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:39 |
| S36 | 29 | "714"/\$.ccls. and (broadcast\$3) and (pattern with matching) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2006/12/21 11:40 |
| S37 | 2174 | (370/432,485,486,487,492).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:23 |
| S38 | 9303 | (709/217,218,219).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 13:30 |
| S39 | 0 | S37 and (input with devices with register\$3 with broadcast\$3 with (blueprint design model convention pattern)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:02 |

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| S40 | 0 | S37 and (input with devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:02 |
| S41 | 0 | S37 and (devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:02 |
| S42 | 110 | (devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:07 |
| S43 | 1 | (input near2 devices) same register\$3 same broadcast\$3 same (blueprint design model convention pattern) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:07 |
| S44 | 4593 | (370/432,485,486,487,492,462,351).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:23 |
| S45 | 0 | S44 and (controller) with devices with register\$3 with (blueprint design model convention pattern patterns) | USPAT | OR | OFF | 2007/02/02 14:25 |
| S46 | 1 | S44 and (controller) with devices with register\$3 with (blueprint design model convention pattern patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:25 |
| S47 | 1 | S44 and (controller) with devices with (store register\$3 receive) with (blueprint design model convention pattern patterns) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:31 |
| S48 | 1 | S44 and (controller) with devices with (store register\$3 receive) with input with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:31 |
| S49 | 0 | S38 and (controller) with devices with (store register\$3 receive) with input with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:32 |

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| S50 | 12 | (controller) with devices with (store register\$3 receive) with input with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:38 |
| S51 | 3 | (controller) with devices with (store register\$3 receive) with source with output with destination | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO | OR | OFF | 2007/02/02 14:38 |